

Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

244844US2S

SERIAL NO.

New Application

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Kazuhisa SAKIHAMA, et al.

FILING DATE

Herewith

GROUP

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>Z.K.</i>	AA	6,249,410	06/19/01	Ming-Dou KER, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

<i>Z.K.</i>	AW	Albert Z.H Wang, "On-Chip ESD Protection for Integrated Circuits, An IC Design Perspective", page 103 (no date)				
	AX					
	AY					
	AZ					

☐ Additional References sheet(s) attached

Examiner

*Z.R. Itz*

Date Considered

*09/09/2004*

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.